

PXI 9718

Embedded PXI Bus System Controller

Features

- Low Power VIA Ezra CPU 800 MHz
- IEEE488.2 GPIB Controller
- USB2, COM, LPT, 10/100BaseT Ethernet
- Windows 2000/XP, Linux
- LongLife™ through ETX technology

Overview

The PXI 9718 is a full-featured PXI bus Embedded System Controller ideally suited for cost sensitive applications.

The ines PXI 9700 series is built on industry-strength ETX CPU modules. By using industry-strength CPU modules a product lifetime of typically ten (10) years can be achieved. For your PXI bus application this means that there is no need to re-evaluate your application each time a new CPU generation appears.

IEEE488.2 GPIB interface

The on-board GPIB interface allows to integrate GPIB equipment with your PXI test system application without additional costs.

It performs all the basic IEEE-488.1 functions such as talker, listener and system controller. The IEEE-488.2 compatible functions make it fully compliant with the IEEE-488.2 specification. In controller applications, you can control typically up to 15 devices (instruments). If operated as a talker/listener (device) interface it does exchange data and state information with the current controller-in-charge of the GPIB bus. The PXI-9718 lets Windows and Linux programs control GPIB devices.



Specifications

PC Architecture Components

CPU: Embedded VIA low power Ezra-800 processor, 128 KB L1 cache memory in die

BIOS: Award 256 KB Flash memory

System memory: One SO-DIMM socket accepts 64MB up to 512MB SDRAM

2nd cache memory: 64KB on the Ezra processor

Enhanced IDE interface: Two channels supports up to four EIDE devices. BIOS auto-detect, PIO Mode 3 or Mode 4, UDMA33 transfer

FDD interface: Flat-ribbon cable FDD interface on main PCB

Serial Ports: One serial RS232 ports (COM1)

Parallel Port: Parallel port supports SPP/EPP/ECP mode

Keyboard/mouse connector: Supports standard PC/AT keyboard and a PS/2 mouse

Power management: Supports power saving modes including Normal/Standby/Suspend modes, APM 1.2 compliant

Watchdog timer: 62 level timer intervals

USB: Two USB ports

VGA: VIA Twister chip with integrated Savage 2D73D/Video Accelerator, 8/16/32 MB frame buffer with system memory, CRT Modes: 1280x1024@16bpp(60Hz), 1024 x 768@16bpp(85Hz)

Ethernet: RTL 8139 chipset, IEEE 802.3u 100BASE-T Fast Ethernet compatible, Built-in boot ROM

HDD: 80 GB

PXI bus

Compliance: PXI Bus Rev. 2.0, CompactPCI Bus Rev. 3.0

Trigger: PXI trigger lines with flexible trigger protocols

GPIB Interface

IEEE 488.1 Capabilities: AH1, SH1, T/TE5, L/LE3, SR1, RL1, PP1/PP2, DC1, DT1, C1, C2, C3, C4, C5

IEEE 488.2 Capabilities: includes the capability to read the following bus lines:EOI, ATN, SRQ, REN, IFC, NRFD, NDAC, DAV

GPIB Handshake Rate: > 1Mbytes/sec

Environmental and Physical

Form factor: Standard 3U PXI/CompactPCI, 12 HP wide (3-slot)

Operating ambient temperature: 0 to 50°C

Storage temperature: -20 to 80°C

Relative humidity: 5 to 95%, noncondensing

Vibration: Operation: 0.5 GRMS, 5...500 Hz, Non-operation: 1.88 GRMS, 5...500 Hz

Weight (net): 0.6 kg

Ordering Information _____

PXI-9718 - Controller, Software CDROM, Windows XP

Option -B - without operating system installed

On the Web _____

Click www.inesinc.com for more information and resources.



ines Test and Measurement GmbH & Co. KG
31542 Bad Nenndorf · DE (Germany)
Phone +49 5723 916 250
Fax +49 5723 916 252
Web www.inesinc.com

Product, service, or company names used in this document are for identification purposes only and may be trademarks of their respective owners. LabView, NI-488.2, LabWindows, PXI, DASyLab, DIAdem are trademarks or registered trademarks of National Instruments Corp., USA, in the United States and/or other countries. Microsoft, Windows, Windows NT, Windows CE, Windows 2000, Windows 7, Windows Vista, Windows ME, Windows XP, Visual Basic, Visual-C++ are trademarks or registered trademarks of Microsoft Corporation in the United States and/or other countries.

All specifications are subject to change without prior notice.